



PATENT
Docket No.: APPL-P2834DVB

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2661

Examiner: Not Yet Assigned

Serial No.: 10/635,706

Filed: August 5, 2003

In re Application of: Jerrold V. Hauck et al.

For: METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 3, 2004, Signed 

Paul J. McCoy

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed please find the following:

1. Information Disclosure Statement;
2. Form PTO-1449
3. Copies of 8 References.

In the event any additional fee is required for filing the above-noted document, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing of attached document timely, the Assistant Commissioner is hereby authorized to charge the fee to our Deposit Account No.: 50-0612.

Respectfully submitted,
SIERRA PATENT GROUP, LTD.

Dated: February 3, 2004

Sierra Patent Group, Ltd.
P.O. Box 6149
Stateline, NV 89449
(775) 586-9500

John W. Crosby
Reg. No.: 49,058



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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Each item of information listed in the attached FORM PTO-1449, for which a copy of each is attached, may be material to the examination of the above-identified application and is, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98. The Examiner is requested to make these items of official record in this application.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that any one or more of these items constitutes prior art.

I

This statement is filed pursuant to:

37 C.F.R. §1.97(b).

This information disclosure statement is filed either (1) within three months of the filing date of the national applications; (2) within three months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application; or (3) before the mailing date of a first office action on the merits, whichever event occurs last.

Accordingly, this information disclosure statement requires no fee and no certification.

37 C.F.R. §1.97(c).

This information disclosure statement is filed after the period specified in 37 C.F.R. §1.97(b), but before the mailing date of either (1) a final action under 37 C.F.R. §1.113 or (2) a notice of allowance under 37 C.F.R. §1.311.

Accordingly, this information disclosure statement requires either the fee specified in 37 C.F.R. § 1.17 (p) or a statement according to 37 C.F.R. §1.97(e).

37 C.F.R. §1.97(d).

This information disclosure statement is filed after the period specified in 37 C.F.R. §1.97(c) and filed on or before the payment of the issue fee and is accompanied by

- (1) The statement specified in paragraph (e) of this section; and
- (2) The fee set forth in §1.17(p).

Accordingly, this information disclosure statement requires the fee specified in 37 C.F.R. §1.17(p), \$180.00, for submission of an information disclosure statement under 37 C.F.R. §1.97(d).

37 C.F.R. §1.97(e).

(1) Each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement.

(2) No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. §1.56(c), more than three months prior to the filing of the statement.

If this statement crosses in the mail with an office action, or is otherwise not in the indicated category of 37 C.F.R. §1.97, it is respectfully requested that this statement be treated in the next appropriate category and made of record. To the extent required, please treat this paper as a conditional petition for acceptance of the information disclosure statement.

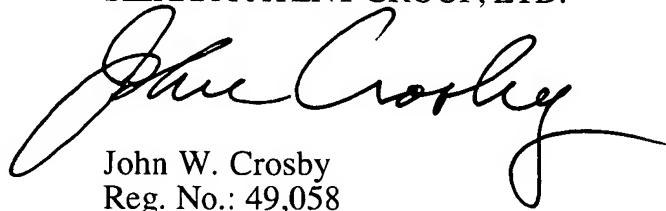
II

(X) No fee is due.

() The fee specified in 37 C.F.R. §1.17(p) for submission of an information disclosure statement under 37 C.F.R. §1.97(c) or 37 C.F.R. § 1.97(d) is enclosed, \$180.00.

In the event any fee is required for filing the above-noted document, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing attached document timely, the Assistant Commissioner is hereby authorized to charge the fee to our Deposit Account No. 50-0612. A duplicate of this page is enclosed.

Respectfully submitted,
SIERRA PATENT GROUP, LTD.



John W. Crosby
Reg. No.: 49,058

Dated: February 3, 2004

Sierra Patent Group, Ltd.
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Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office					Atty. Docket No. APPL-P2834DVB	Serial No. 10/635,706	
Information Disclosure Statement by Applicant					Applicant: Jerrold V. Hauck et al.		
(Use several sheets if necessary)					Filed: August 5, 2003	Group: 2661	
U.S. Patent Documents							
Init.		Document No.	Date	Name	Class	Subclass	Filing Date
	1	6,426,962 B1	07/30/2002	Cabezas et al.	370	516	06/24/1998
Foreign Documents							
Translation							
Init.		Document No.	Date	Country	Class	Subclass	Yes <input type="checkbox"/> No <input checked="" type="checkbox"/>
	2	EP 1 085 706 A2	03/21/2001	Europe	H04L	12/46	<input type="checkbox"/> <input checked="" type="checkbox"/>
	3	EP 1 085 706 A3	10/30/2002	Europe	H04L	12/46	<input type="checkbox"/> <input checked="" type="checkbox"/>
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)							
	4	Bregni et al., "Jitter Testing Technique and Results at VC-4 Desynchronizer Output of SDH Equipment, <u>IEEE International Conference on Communications</u> , Vol. 3, pp. 1407-1410, May 12, 1994.					
	5	"Information technology-Microprocessor systems-Control and Status registers (CSR) Architecture for microcomputer buses", [ANSI/IEEE Std 1212, 1994 Ed.], <u>The Institute of Electrical and Electronics Engineers, Inc.</u> , pp. 1-122, October 5, 1994.					
	6	Bregni et al., "Jitter Testing Technique and Results at VC-4 Desynchronizer Output of SDH Equipment, <u>IEEE Transactions on Instrumentation and Measurement</u> , Vol. 44, Issue 3, pp. 675-678, June 1995.					
	7	Shiwen et al., "Parallel positive justification in SDH C-4 mapping, <u>IEEE International Conference on Communications</u> , Vol. 3, pp. 1577-1581, June 12, 1997					
	8	'Information Technology-Fibre Channel-Methodologies for Jitter Specification", <u>NCITS Technical Report (NCITS TR-25-1999, REV 10)</u> , pp. 1-96, June 9, 1999.					
Examiner					Date Considered		
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.							